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Customer No.: 31561
Docket No.: 9720-US-PA
Application No.: 10/604,744

In The Abstract

A printed circuit board [[comprises]]includes a plurality of patterned circuit layers, a plurality of insulation layers and a plurality of circuits. Each insulation layer is located between a pair of neighboring patterned circuit layers for isolating the patterned circuit layers. The insulation layer and the patterned circuit layers together form a laminated layer. The circuits are formed on the sidewalls of the printed circuit board or the interior sidewalls of a cavity or an opening within the printed circuit board for interconnecting various patterned circuit layers electrically.